

HFD2101-2 Integrated Anti- Jamming Receiver



Product Technical Manual



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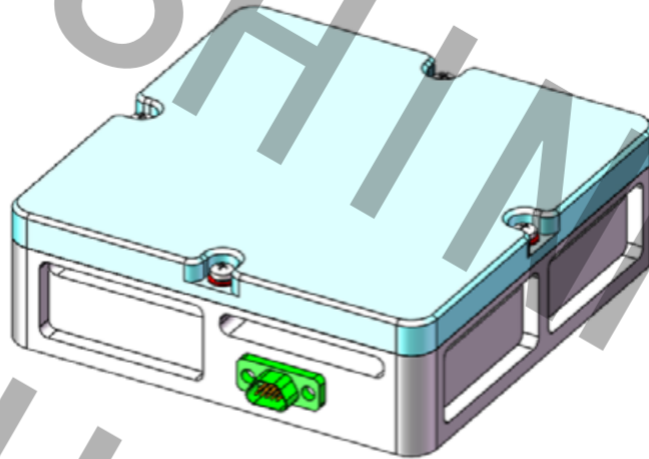
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1.Overview

The FD2101-2 Integrated Anti-Jamming Receiver (as the "Anti-Jamming Receiver") is designed for suppressing jamming in the BDS-B1 and GPS-L1 frequency bands. It features positioning capability for GPS+BDS B1, and can resist complex suppression jamming such as broadband, pulse, narrowband and frequency-sweeping jamming.



Integrated Anti-Jamming Receiver Design Diagram

2.Features

Low Power Consumption: Adopting highly integrated design such as chipization, the total power consumption of the entire antenna is less than 10W, significantly lower than traditional anti-jamming receivers.

Lightweight: With an integrated design, the total weight of the unit is less than 500g.



Integrated Anti-Jamming Receiver

3.Applications

It can be installed on fixed-wing UAVs, unmanned ships, various motor vehicles,etc., ensuring continuous and reliable positioning, velocity measurement and time service in strong electromagnetic countermeasure environments.



Fixed-wing UAV

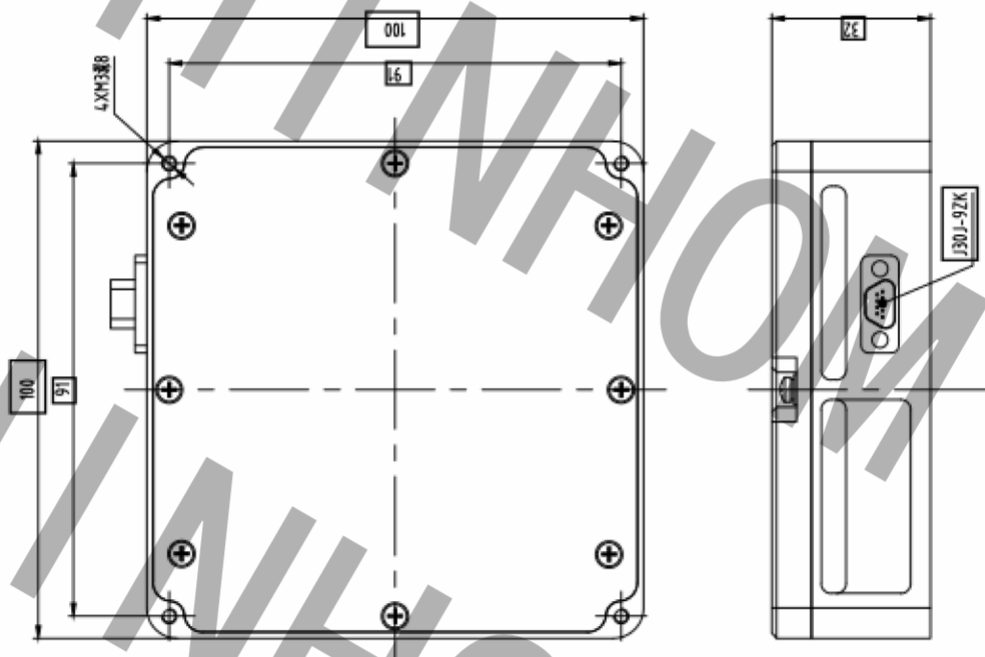
4.Technical Specifications

Item	Parameter
Receiving Frequency	GPS- L1, BDS- B1I/B1C
Positioning Accuracy	Horizontal 5m (1 σ), Elevation 10m (1 σ)
Speed Accuracy	0.2 m/s (1 σ)
Positioning Update Rate	10 Hz
Anti- Jamming Frequency	BDS- B1 / GPS- L1
Anti- Jamming Capability	Single ≥ 100 dB, Triple ≥ 90 dB
Input Voltage	9 - 36 V
Power Consumption	≤ 10 W
Weight	≤ 500 g
Operating Temperature	-40°C ~ 75°C
Storage Temperature	-55°C ~ 85°C

5.Interface Definition

Pin	Name	Description
1	VCC IN	5V or 9 – 36VDC Power Supply
2	VCC IN	5V or 9 – 36VDC Power Supply
3	GND	Ground
4	GND	Ground
5	RS422A_RX+	RS422 Receive +
6	RS422A_RX-	RS422 Receive -
7	RS422A_TX+	RS422 Transmit +
8	RS422A_TX-	RS422 Transmit -
9	NC	No Connection

6.Outline Dimensions



Anti-jamming Receiver Outline Dimensions

Dimensions: 100mm × 100mm × 32mm with tolerance $\pm 0.3\text{mm}$.